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MullerRESPONSE UNDER 37 C.F.R. 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2815

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ference et al.

Confirmation No.: 9501

Serial No: 09/732,240 ✓

Examiner: Chu, Chris C.

Filed: 12/07/00

Group Art Unit: 2815

Title: MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING SELF-ALIGNING ELECTRICAL CONTACT ARRAY

Entered
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2/3/03Certificate of Facsimile Transmission

I hereby certify that this correspondence is being sent via facsimile transmission to the U.S. Patent and Trademark Office at fax number 703-308-7722 to Box AF, Commissioner for Patents, Washington, D.C. 20231, on January 13, 2003.

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Date of Signature: January 13, 2003Box AF
Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

Response to Final Office Action

Dear Sir:

This paper is submitted in reply to the final Office Action mailed October 23, 2002, in connection with the above-referenced application. Reconsideration and allowance of all claims are respectfully requested in view of the Amendments and Remarks below.

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